EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	45	(Peter near2 Camble).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:18
S2	99	(Stephen near2 Gold).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:18
S3	166	(Ian near2 Peter).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:18
S4	26	(Ian near2 Crighton).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:19
S5	54	(Curtis near2 Ballard).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:19
S6	56501	(restrict$4 or secur$4) near3 acces$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 13:29
S7	1256	media near2 library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:39
S8	425526	partition$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:20
S9	1198468	ID or identification	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:59

EAST Search History

S10	71618	barcode$2 or (bar adj code)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:21
S11	17	S6 same S7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:43
S12	14	S11 and S9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:21
S13	2	S12 and S10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:23
S14	1	S8 and S13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:22
S15	6159	S6 same S9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:24
S16	29	S15 and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:24
S17	9	S16 and S10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:25
S18	1	S8 and S17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:24
S19	4	("20010034813" or "6725394").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:45

EAST Search History

S20	1	S9 and S19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 13:45
S21	1201445	ID or identification$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:42
S22	1198468	S9 same S21	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:04
S23	6194	S6 same S21	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:00
S24	11	S7 same S23	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:01
S25	1	S19 and S21	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:18
S26	616	(serial adj number$2) same library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 13:24
S27	40296	(serial adj number$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:10
S28	2244272	drive	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:10
S29	3288730	drive$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:11

EAST Search History

S30	11	S7 same S27	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:11
S31	94590	(restrict$4 or secur$4 or prevent$4) near3 acces$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:22
S32	6	S26 same S31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:29
S33	250	S26 and S31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:30
S34	571495	media	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:30
S35	94	S33 and S34	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:30
S36	45	S33 and S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:41
S37	5149	(ID or identification$2 or (serial adj number$4)) near2 drive$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:42
S38	510	library adj controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:27
S39	518	library adj (controler or controller or controller$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 14:43

EAST Search History

S40	18	S37 and S39	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:07
S41	1118	media adj (id or identification or serial)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:23
S42	10	S39 and S41	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:14
S43	4	S39 same S41	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:14
S44	26	S31 same S41	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:15
S45	0	S44 and S39	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:15
S46	17	S29 and S44	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:15
S47	106407	(den$4 or restrict$4 or secur$4 or prevent$4) near3 acces$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:22
S48	37052	(match$4 or "same" or identical) near2 (id or identification or serial)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:24
S49	391	S47 same S48	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:29

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S50	2	S49 and S39	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:25
S51	37857	(tape or magnetic) adj drive$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:26
S52	26	S49 and S51	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:26
S53	17258	raid or dasd	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:27
S54	0	S49 same S53	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:27
S55	130397	library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:27
S56	1	S49 same S55	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:27
S57	4662	S47 and S48	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:30
S58	313	S51 and S57	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:31
S59	2	S7 and S58	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:30

EAST Search History

S60	313	S51 and S58	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:33
S61	128	S55 and S58	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:34
S62	42435	cartridge$2 same drive$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 11:37
S63	616	(serial adj number$2) same library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 11:40
S64	122	S62 and S63	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 11:41
S65	321845	eject$4 or evict$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 11:41
S66	33	S64 and S65	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 12:36
S67	2	"5455409".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 12:36
S68	1	S65 and S67	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 12:36
S69	6966	((serial adj number$2) or id or identification) same (match$4 or compar$4 or identical) same library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 13:34

EAST Search History

S70	56708	(restrict$4 or secur$4) near3 acces$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:42
S71	210	S69 and S70	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 13:30
S72	4	S65 and S71	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 13:30
S73	13	((serial adj number$2) or id or identification) same (match$4 or identical) same library same cartridg$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:39
S74	723	((serial adj number$2) or id or identification) same (match$4 or identical) same (EEPROM or eprom or prom)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:41
S75	1256	media near2 library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:41
S76	1	S74 and S75	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:40
S77	1070	((serial adj number$2) or id or identification) same (match$4 or identical) same (EEPROM or eprom or prom or flash)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:41
S78	130799	library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:41
S79	116	S77 and S78	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:41

EAST Search History

S80	28	S70 and S79	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 14:42

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#4	(media library<IN>metadata)
#5	(library controller<IN>metadata)
#6	(library controller<IN>metadata)
#7	(library partition<IN>metadata)
#8	library and (ID or identification)
#9	(secured access*<IN>metadata)
#10	((protect* memory area)<in>metadata)
#11	((protect* memory area)<in>metadata)
#12	((media library<IN>metadata)) <AND> ((((protect* memory area)<in>metadata)) <AND> ((secured access*<IN>metadata)))
#13	restricted access
#14	(((media library<IN>metadata)) <AND> ((((protect* memory area)<in>metadata)) <AND> ((secured access*<IN>metadata)))) <AND> (restricted access)

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1 Programming languages for mobile code

Tommy Thorn

September 1997 **ACM Computing Surveys (CSUR)**, Volume 29 Issue 3

Publisher: ACM Press

Full text available: pdf(393.65 KB) Additional Information: full citation, abstract, references, citings, index terms, review

Sun's announcement of the programming language Java more that anything popularized the notion of mobile code, that is, programs traveling on a heterogeneous network and automatically executing upon arrival at the destination. We describe several classes of mobile code and extract their common characteristics, where security proves to be one of the major concerns. With these characteristics as reference points, we examine six representative languages proposed for mobile code. The conclusion ...

Keywords: Java, Limbo, Objective Caml, Obliq, Safe-Tcl, distribution, formal methods, mobile code, network programming, object orientation, portability, safety, security, telescript

2 Protection and the control of information sharing in multics

Jerome H. Saltzer

July 1974 **Communications of the ACM**, Volume 17 Issue 7

Publisher: ACM Press

Full text available: pdf(1.75 MB) Additional Information: full citation, abstract, references, citings, index terms

The design of mechanisms to control the sharing of information in the Multics system is described. Five design principles help provide insight into the tradeoffs among different possible designs. The key mechanisms described include access control lists, hierarchical control of access specifications, identification and authentication of users, and primary memory protection. The paper ends with a discussion of several known weaknesses in the current protection mechanism design.

Keywords: Multics, access control, authentication, computer utilities, descriptors, privacy, proprietary programs, protected subsystems, protection, security, time-sharing systems, virtual memory

3 Architecture for Protecting Critical Secrets in Microprocessors

Ruby B. Lee, Peter C. S. Kwan, John P. McGregor, Jeffrey Dwoskin, Zhenghong Wang
May 2005 **ACM SIGARCH Computer Architecture News , Proceedings of the 32nd Annual International Symposium on Computer Architecture ISCA '05**, Volume 33 Issue 2
Publisher: IEEE Computer Society, ACM Press
Full text available: pdf(143.62 KB) Additional Information: full citation, abstract, index terms

We propose "secret-protected (SP)" architecture to enable secure and convenient protection of critical secrets for a given user in an on-line environment. Keys are examples of critical secrets, and key protection and management is a fundamental problem ¿ often assumed but not solved ¿ underlying the use of cryptographic protection of sensitive files, messages, data and programs. SP-processors contain a minimalist set of architectural features that can be built into a general-purpose microprocess ...

4 Formal Models for Computer Security

Carl E. Landwehr
September 1981 **ACM Computing Surveys (CSUR)**, Volume 13 Issue 3
Publisher: ACM Press
Full text available: pdf(2.98 MB) Additional Information: full citation, references, citings, index terms

5 DRM experience: Digital rights management in a 3G mobile phone and beyond

Thomas S. Messerges, Ezzat A. Dabbish
October 2003 **Proceedings of the 3rd ACM workshop on Digital rights management DRM '03**
Publisher: ACM Press
Full text available: pdf(306.59 KB) Additional Information: full citation, abstract, references, citings, index terms

In this paper we examine how copyright protection of digital items can be securely managed in a 3G mobile phone and other devices. First, the basic concepts, strategies, and requirements for digital rights management are reviewed. Next, a framework for protecting digital content in the embedded environment of a mobile phone is proposed and the elements in this system are defined. The means to enforce security in this system are described and a novel "Family Domain" approach to content management ...

Keywords: MPEG-21, copyright protection, cryptography, digital content, digital rights management, embedded system, key management, mobile phone, open mobile alliance, security

6 Data Security

Dorothy E. Denning, Peter J. Denning
September 1979 **ACM Computing Surveys (CSUR)**, Volume 11 Issue 3
Publisher: ACM Press
Full text available: pdf(1.97 MB) Additional Information: full citation, references, citings, index terms

7 Mondrian memory protection

Emmett Witchel, Josh Cates, Krste Asanović
October 2002 **ACM SIGPLAN Notices , ACM SIGARCH Computer Architecture News , ACM SIGOPS Operating Systems Review , Proceedings of the 10th international conference on Architectural support for programming languages and operating systems ASPLOS-X**, Volume 37 , 30 , 36 Issue 10 , 5 , 5

Publisher: ACM Press
Full text available: pdf(1.53 MB) Additional Information: full citation, abstract, references, citings

Mondrian memory protection (MMP) is a fine-grained protection scheme that allows multiple protection domains to flexibly share memory and export protected services. In contrast to earlier page-based systems, MMP allows arbitrary permissions control at the granularity of individual words. We use a compressed permissions table to reduce space overheads and employ two levels of permissions caching to reduce run-time overheads. The protection tables in our implementation add less than 9% overhead to ...

8 Integrating security in a large distributed system
M. Satyanarayanan
August 1989 **ACM Transactions on Computer Systems (TOCS)**, Volume 7 Issue 3
Publisher: ACM Press
Full text available: pdf(2.90 MB) Additional Information: full citation, abstract, references, citings, index terms, review

Andrew is a distributed computing environment that is a synthesis of the personal computing and timesharing paradigms. When mature, it is expected to encompass over 5,000 workstations spanning the Carnegie Mellon University campus. This paper examines the security issues that arise in such an environment and describes the mechanisms that have been developed to address them. These mechanisms include the logical and physical separation of servers and clients, support for secure communication ...

9 Security: SECA: security-enhanced communication architecture
Joel Coburn, Srivaths Ravi, Anand Raghunathan, Srimat Chakradhar
September 2005 **Proceedings of the 2005 international conference on Compilers, architectures and synthesis for embedded systems CASES '05**
Publisher: ACM Press
Full text available: pdf(396.53 KB) Additional Information: full citation, abstract, references, index terms

In this work, we propose and investigate the idea of enhancing a System-on-Chip (SoC) communication architecture (the fabric that integrates system components and carries the communication traffic between them) to facilitate higher security. We observe that a wide range of common security attacks are manifested as abnormalities in the system-level communication traffic. Therefore, the communication architecture, with its global system-level visibility, can be used to detect them. The communicati ...

Keywords: AMBA Bus, access control, architecture, attacks, bus, communication, digital rights management (DRM), intrusion detection, security, security-aware design, small embedded systems, system-on-chip (SoC)

10 Processor microarchitecture II: AEGIS: architecture for tamper-evident and tamper-resistant processing
G. Edward Suh, Dwaine Clarke, Blaise Gassend, Marten van Dijk, Srinivas Devadas
June 2003 **Proceedings of the 17th annual international conference on Supercomputing**
Publisher: ACM Press
Full text available: pdf(286.90 KB) Additional Information: full citation, abstract, references, citings, index terms

We describe the architecture for a single-chip aegis processor which can be used to build computing systems secure against both physical and software attacks. Our architecture assumes that all components external to the processor, such as memory, are untrusted. We show two different implementations. In the first case, the core functionality of the operating system is trusted and implemented in a security kernel. We also describe a variant implementation assuming an untrusted operating s ...

Keywords: certified execution, secure processors, software licensing

11 Computing curricula 2001

September 2001 **Journal on Educational Resources in Computing (JERIC)**

Publisher: ACM Press

Full text available: pdf(613.63 KB) html(2.78 KB) Additional Information: full citation, references, citings, index terms

12 Computers and Privacy: A Survey

Lance J. Hoffman

June 1969 **ACM Computing Surveys (CSUR)**, Volume 1 Issue 2

Publisher: ACM Press

Full text available: pdf(1.74 MB) Additional Information: full citation, references, citings, index terms

13 Incremental cryptography and application to virus protection

Mihir Bellare, Oded Goldreich, Shafi Goldwasser

May 1995 **Proceedings of the twenty-seventh annual ACM symposium on Theory of computing**

Publisher: ACM Press

Full text available: pdf(1.65 MB) Additional Information: full citation, references, citings, index terms

14 Securing a global village and its resources: baseline security for interconnected signaling system #7 telecommunications networks

Hank M. Kluepfel

December 1993 **Proceedings of the 1st ACM conference on Computer and communications security**

Publisher: ACM Press

Full text available: pdf(1.19 MB) Additional Information: full citation, abstract, references, index terms

The resulting national focus on Network Integrity issues, spawned the development of an industry commitment to affect and realize a minimum security baseline for interconnected SS7 networks. In addition the affected carriers in those outage have accelerated their pursuit of secure solutions to today's intelligent networking.[2]This paper will focus on the development of the baseline and the current effort to take the baseline into national, e.g., National Ins ...

15 Computer security: a survey

Peter S. Browne

September 1972 **ACM SIGMIS Database**, Volume 4 Issue 3

Publisher: ACM Press

Full text available: pdf(1.04 MB) Additional Information: full citation, references

16 Security analysis: Security considerations for IEEE 802.15.4 networks

Naveen Sastry, David Wagner

October 2004 **Proceedings of the 2004 ACM workshop on Wireless security**

Publisher: ACM Press

Full text available: pdf(175.00 KB) Additional Information: full citation, abstract, references, citings, index terms

The IEEE 802.15.4 specification outlines a new class of wireless radios and protocols targeted at low power devices, personal area networks, and sensor nodes. The specification includes a number of security provisions and options. In this paper, we highlight places where application designers and radio designers should exercise care when implementing and using 802.15.4 devices. Specifically, some of the 802.15.4 optional features actually reduce security, so we urge implementors to ignore those ...

Keywords: IEEE 802.15.4, link layer security, sensor networks

17 Session summaries from the 17th symposium on operating systems principle (SOSP'99)

Jay Lepreau, Eric Eide
April 2000 **ACM SIGOPS Operating Systems Review**, Volume 34 Issue 2
Publisher: ACM Press
Full text available: pdf(3.15 MB) Additional Information: full citation, index terms

18 Computer Software and Copyright

Calvin N. Mooers
March 1975 **ACM Computing Surveys (CSUR)**, Volume 7 Issue 1
Publisher: ACM Press
Full text available: pdf(2.63 MB) Additional Information: full citation, references, citings, index terms

19 Security issues for wireless ATM networks

Danai Patiyoot
January 2002 **ACM SIGOPS Operating Systems Review**, Volume 36 Issue 1
Publisher: ACM Press
Full text available: pdf(1.75 MB) Additional Information: full citation, abstract, references, index terms

To be able to fulfil the need of user in wireless ATM, the system has to acquire features. One of the system features for the wireless ATM is functionality especially the security aspect. There is so far tittle, if not none, security consideration in the developing of wireless ATM standard. Therefore a wide range of features in security functions is in consideration. This paper tried to define the features of security in wireless ATM networks considering it features from existing fixed ATM netwo ...

Keywords: security, wireless ATM

20 RAID: high-performance, reliable secondary storage

Peter M. Chen, Edward K. Lee, Garth A. Gibson, Randy H. Katz, David A. Patterson
June 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 2
Publisher: ACM Press
Full text available: pdf(3.60 MB) Additional Information: full citation, abstract, references, citings, index terms, review

Disk arrays were proposed in the 1980s as a way to use parallelism between multiple disks to improve aggregate I/O performance. Today they appear in the product lines of most major computer manufacturers. This article gives a comprehensive overview of disk arrays

and provides a framework in which to organize current and future work. First, the article introduces disk technology and reviews the driving forces that have popularized disk arrays: performance and reliability. It discusses the tw ...

Keywords: RAID, disk array, parallel I/O, redundancy, storage, striping

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